In the Abstract:

Please replace the Abstract with the following amended Abstract:

There is provided a A multiple-processor information processing system-which is capable of reducing adverse influence of overload of a given communication process on other communication processes than the given communication process. In this multiple-processor information processing system, a virtual IP address definition section defines virtual IP addresses on a process module-by-processor module basis. A storage device stores the virtual IP addresses defined by the virtual IP address definition section and information indicative of ones of the processor modules corresponding to the virtual IP addresses, respectively, in a state correlated with each other. A notification section notifies a router of a virtual IP address of each processor module and a real IP address of the each process module as routing information, for the each processor module having the virtual IP address stored in the storage device.